

**IN THE ABSTRACT**

Please delete the current Abstract:

~~The device comprises on the same electronic chip frequency transposition means (MX) connected to a local main oscillator VCOP. The main oscillator VCOP is incorporated inside a main phase locked loop PLL2 whereof the reference frequency is supplied by a voltage controlled auxiliary oscillator VCOA, itself incorporated into an auxiliary phase locked loop PLL1 whereof the reference frequency is less than the frequency of the auxiliary oscillator. The reference frequency SRFP of the main loop is less than the output frequency of the main oscillator, greater than 10 times the frequency spacing of the channels reduced to the output frequency of the main oscillator, and removed by a whole multiple of the send or receive frequency from at least the cut-off frequency of the main loop.~~

~~Reference: Figure 3.~~

Please substitute the following new Abstract:

A frequency transposition device is connected to a local main oscillator. The main oscillator is incorporated inside a main phase locked loop whereof the reference frequency is supplied by a voltage-controlled auxiliary oscillator, which is itself incorporated into an auxiliary phase locked loop. The reference frequency of the auxiliary phase locked loop is less than the frequency of the auxiliary oscillator for the main phase locked loop. The reference frequency of the main phase locked loop is less than the output frequency of the main oscillator, is greater than 10 times the frequency spacing of the channels reduced to the output frequency of the main oscillator, and is removed by a whole multiple of

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the send or receive frequency from at least the cut-off frequency of the  
main phase locked loop.

A copy of the new Abstract is presented on a separate sheet of paper attached hereto. 37

CFR 1.72.